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A Method of Designing in Microstrip Equalizer Based on the Substructure Method

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Abstract: To make the optimization design result of system and components which, contain ordinary non-uniform substructures, meet the measurement criteria better. According to the microstrip equalizer's complex electromagnetic characteristics, the Substructure Analyzing Method of Interconnect-net based on parameters matrices measurement database is proposed and verified which combines the measure technology with database. The analysis and engineering design of the abnormal-edge microwave apparatus is realized. The method treats the complex microwave structure as a interconnect of some simpler substructure, uses the measurement database to character the substructure whose electricity and magnetism property is difficult to exactly resolve and uses the net cascading method to get the overall net parameters. The method is the basis of a number of current and pending patents. It may also lead to other fields such as the design of shortwave multimode multifeed antenna system. In this paper, a typical example of microstrip equalizer optimization is given to illustrate the efficiency of the method.

Keywords: Microstrip equalizer, method of substructure, optimization.

1. INTRODUCTION

Equalizer is one type of microwave passive component used widely in modern radar system, high accuracy microwave measure system and other microwave application system [1]. It can compensate various waves and irregular change of the system amplitude and phase characteristic and makes the system work in a linear transmission state.

Since 70's, the Microwave CAD, which combines microwave and computer technology, has affected the development of the microwave technology to a great degree. With the progress of software and hardware of computer, the combination of numerical method using high-speed computer technology and Electromagnetism resolution method has solved a great deal of questions of the application domain, especially the analyzing and designing of electromagnetic components. Several years ago, we brought forward the substructure analyzing method of interconnect-net [2], based on great capacity database, to settle the question that we encountered while research, for example the difficulty in analyzing and optimizing the complex electromagnetic structure, the certain blindness in designing and machining [3].

Many patents on microstrip transmission lines were invented for Equalizer [4-9]. For example, An equalizer circuit for equalizing RF frequencies was implemented as an MMIC or MIC having a series of microstrip transmission lines between an RF input terminal and an RF output terminal. It had at least one shunt path which comprised of a series combination of an FET, which was able to switch between a conducting state and a nonconducting state and a stud tuner microstrip transmission line [4]. Another equalizer circuit for RF frequencies comprised of a microstrip transmission line having a stub tuner extending transversely and having resistive means coupled to the stub tuner to de-Q the stub tune [9]. In this paper, the Substructure Analyzing Method of Interconnect - net is applied to micro-stripe equalizer's design, the theory plot the complex communication system by cascading of a set of simple substructures., The design and set up of the effective and accurate parameter measurement database of different substructure and by the use of some technologies such as transform and cascade, the micro-stripe equalizer will be designed and optimized. It is a simple, efficient and quick method. We carry out the optimization design of a kind of micro-stripe equalizer to illustrate the use of the substructure analyzing method.

2. THE CONCEPT OF EQUILIBRIUM

The idea of equilibrium is: if any part of signal transport system cannot transport signal without aberrance, the signal will change. To avoid this, we must design some network whos transport characteristics are complementary with the original system [10]. As a result, the whole system can transfer the signal without any change. The whole process is called equilibrium, the component or network called equalizer. Neural network techniques display more accuracy for price forecasting compared to the ARIMA models. Neural networks and SVM techniques are data driven and simple, where, they can approximate nonlinear function. These can be powerful and flexible tools for forecasting, if provided with enough data for training and an adequate selection.

3. OVERVIEW OF THE SUBSTRUCTURE ANALYZ-ING METHOD OF INTERCONNECT-NET

We define common network as: it is a kind of abstract physical model, formed with inter-connecting of network unit, converting physical quantity between their ports, so network unit is an abstract concept relative to physical part, it obeys corresponding physical laws [11]. Usually, we regard microwave net as electric net which works in microwave band. After the 1940's, microwave technology came forth as an important way of researching the electrical characteristics, since then the microwave net analyzing method has been developing rapidly. Recently, as a result of the progress of information and electronics, especially use of computers widely, microwave network is advancing more quickly than ever and many new concept, theory and method are appearing [12]. The substructure analyzing method of interconnect-net is based on the microwave engineering experience, technology of software and hardware of computer and microwave's highly precise measurement. It analyzes net using substructure idea and it's points are as follow:

1. Treat the complex net as the cascading of a set of substructures;

2. With the substructure database model, use multiple methods (which include the database, resolution and numerical method and the equivalent circuit etc.) to characterize the net property of substructures. Database is a basic method here;

3. Using the net cascading method to get the overall net parameters;

4. With the direct use of the database data and the use of some technologies such as data lookup, data interpolation, database self-completeness, data pre-compute and data visualization, the net analyzing will be performed effectively;

5. Based on the analysis, optimization design can be achieved with the use of complex optimization method and the tact-select method, process trace technology.

The original intention of this method is to solve the analysis and optimization of some complex microwave net, it can also be used in the physical net which has the similar model [13]. And compared with the existing methods, the Substructure Analyzing Method of Interconnect-net brings some new ideas:

1. It can solve the analyses and optimization of some complex but parameter measurable net;

2. It can increase the rate of analyzing and optimization of the design of some gradually changing structures greatly.

The substructure analyzing method of interconnect-net succeeded in combining microwave engineering with computer technology (database, view-data), and representing the net cell by measuring database directly. That method can analyze, optimize and design a kind of non-uniform net in engineering [14]. In this paper, we put it for optimization and design the micro-stripe equalizer.



Fig. (1). The dual port s-parameter.

4. THE FUNDAMENTAL OF SETTING OFF SUB-STRUCTURE AND CASCADE

4.1. Setting of Substructure

Substructure can be basic net unit or a set of net unit due to different objects. The aim is to make analysis more quick and calculation more precise, for the convenience of debugging, designing and the optimization of the system performance [15]. The main point is which definite electronicmagnetism characteristic or net parameter be used [16].

Usually, we use the S-parameter to describe the port characteristic of a certain substructure, for it has some merits as follow:

1. Easy to measure and use.

2. S-parameter can calculate circuit with Smith chart.

3. When the reference plane moves, the change of S-parameter is reflected on phase only.

4. It is easy to deduce the S-parameter of multiple ports network.

S-parameter of typical dual port net figure in Fig. (1). The calculating formula of S-parameter of typical dual port network is follow:

$$\begin{vmatrix} s_{11} = \frac{b_1}{a_1} \\ a_{12} = \frac{b_1}{a_2} \\ a_{1} = \frac{b_1}{a_2} \\ a_{1} = \frac{b_1}{a_2} \\ a_{1} = \frac{b_2}{a_1} \\ a_{2} = \frac{b_2}{a$$

eflectance of port 2

4.2. Cascade of Substructure

It is convenient to get the parameter of overall net by using A-parameter. The calculating formula of A-parameter of typical dual port network is follow:



Fig. (2). The microstrip equalizer configuration.



Fig. (3). The model of substructure interconnected.

$$\begin{cases} a_{11} = \frac{u_1}{u_2} |_{i_2=0} & \text{output opening, the reciprocal of} \\ \text{normalize voltage transmission coefficient} \\ a_{12} = \frac{u_1}{u_2} |_{u_2=0} & \text{output shorting, the} \\ \text{normalize transfer admittance of input and output} \\ a_{21} = \frac{i_1}{u_2} |_{i_2=0} & \text{output opening, the} \\ \text{normalize transfer admittance of input and output} \end{cases}$$
(2)

$$a_{22} = -\frac{l_1}{l_2}|_{u_2=0}$$
 output shorting, the

reciprocal of normalize current transmission coefficient

If n substructures cascade, we can use formula as follow to get the overall A-parameter:

$$[a]_{all} = [a]_1 \bullet [a]_2 \bullet \dots \bullet [a]_n = \prod_{i=1}^n [a]_i$$
(3)

4.3. The Connection Between S-parameter and A-parameter

During calculating, S-parameter and A-parameter can interconvert by using formula as fellow:

$$\begin{cases} s_{11} = \frac{a_{11} + a_{12} - a_{21} - a_{22}}{a_{11} + a_{12} + a_{21} + a_{22}} \\ s_{22} = \frac{a_{22} + a_{12} - a_{21} - a_{11}}{a_{11} + a_{12} + a_{21} + a_{22}} \\ s_{12} = \frac{2}{a_{11} + a_{12} + a_{21} + a_{22}} \\ s_{21} = \frac{2(a_{11}a_{22} - a_{21}a_{12})}{a_{11} + a_{12} + a_{21} + a_{22}} \\ \end{cases}$$

$$\begin{cases} a_{11} = \frac{1}{2}[s_{12} + \frac{(1 + s_{11})(1 - s_{22})}{s_{21}}] \\ a_{12} = \frac{1}{2}[s_{12} - \frac{(1 + s_{11})(1 - s_{22})}{s_{21}}] \\ a_{21} = -\frac{1}{2}[s_{12} - \frac{(1 - s_{11})(1 - s_{22})}{s_{21}}] \\ a_{22} = \frac{1}{2}[s_{12} + \frac{(1 - s_{11})(1 - s_{22})}{s_{21}}] \end{cases}$$

$$(5)$$

5. ANALYZING THE STRUCTURE OF MICROSTRIP EQUALIZER AND CARVING UP NET

As to the microstrip equalizer shown in Fig. (2), a common method of constructing substructure is shown by broken lines in Fig. (3), then forming a inter-connect net which is constituted by several substructure cascades. If each sub-



Fig. (4). The optimization result.

structure is figured by a database of S-parameter then this analyzing module is consists of several database cascades [17].

To construct database of substructure, a great deal of data is necessary. There are three ways that we can get data needed:

1. Producing some substructure components and measuring in test system to get data.

2. Using advanced EDA software (ADS, HFSS, etc) to simulate some substructure.

3. Calculating the relative simple part using resolution formula, for instance: micro-stripe.

6. MATHEMATICAL MODEL OPTIMIZATION AND ONE TYPICAL OPTIMIZATION RESULT

After obtaining the data representation of each substructure, we can use the method of net cascade and optimization to design the overall system. The goal is to approach target curve maximum by adjusting combined mode of various substructure [18].

We can describe certain curves by using some value of power-level, as:

$$P_{in0}(w) = \sum_{i=0}^{n} P_{in} \bullet \delta(f_0 + \frac{B_f}{n}i)$$

$$\delta(f(i)) = \begin{cases} 1 & i \in [0, 1, 2, \dots, n] \\ 0 & others \end{cases}$$
(6)

B_f: bandwidth of working frequency band

f₀: floor level of working frequency band

Pin: value of power-level of each frequency point

The final goal is to minimize the error between output power and the target one at the N frequency points. That is to say:

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$$T(i,\mathbf{L}) = P_c \bullet L_A(i)$$

$$T_0(i) = P_{i,0}(\omega)$$
(7)

$$\mathbf{L} = (\mathbf{L}_{p}, \mathbf{L}_{q}, \mathbf{L}_{r}, \mathbf{L}_{s})$$

= $(Lp_{1}, Lp_{2}, ..., Lp_{m}, Lq_{1}, Lq_{2}, ..., Lq_{m},$ (8)
 $Lr_{1}, Lr_{2}, ..., Lr_{m}, Ls_{1}, Ls_{2}, ..., Ls_{m})$

Make

$$\min_{\mathbf{L}} \max_{i} \left\{ \left(T(i, \mathbf{L}) - T_0(i) \right)^2 \right\}$$
(9)

The standing-wave ratio pin Transmission line should satisfy the following constraint:

$$\max_{i} \{ \boldsymbol{\rho}(i, \mathbf{L}) \} < \boldsymbol{\rho}_{0} \tag{10}$$

i is serial number of frequency point

When we design practically, the length, width and area of substrate and surface area of micro-stripe line can be treated as a constraint condition. All that together, we get the final mathematical model as follows:

$$\min_{\mathbf{L}} \max_{i} \left\{ (T(i, \mathbf{L}) - T_{0}(i))^{2} \right\}
= \sum_{i}^{m_{i}} \left\{ \rho(i, \mathbf{L}) \right\} < \rho_{0} \\
0 \le \mathbf{L} \mathbf{p}_{j} < 5mm \\
0 \le \mathbf{L} q_{j} \le 20mm \qquad i \in [0, 1, 2, ..., n] \\
0 \le \mathbf{L} r_{j} \le 5mm \\
0 \le \mathbf{L} s_{i} \le 15mm$$
(11)

This is a question with nonlinearity constraint, its augmented objective function is:

$$F(\mathbf{L})|_{L=L0} = \min_{\mathbf{L}} \left\{ \max_{i} \{ [T(i, \mathbf{L}) - T_{0}(i)]^{2} \}$$
(12)
+ $M\rho \cdot \{ \min[0, (\rho_{0} - \max_{i} \rho(i, \mathbf{L}))] \}^{2}$
+ $\sum_{j=0}^{N} M_{Lpj} \cdot \{ [\min(0, 5 - Lp_{j})]^{2} + [\min(0, Lp_{j})]^{2} \}$
+ $\sum_{j=0}^{N} M_{Lqj} \cdot \{ [\min(0, 20 - Lq_{j})]^{2} + [\min(0, Lq_{j})]^{2} \}$
+ $\sum_{j=0}^{N} M_{Lrj} \cdot \{ [\min(0, 5 - Lr_{j})]^{2} + [\min(0, Lr_{j})]^{2} \}$
+ $\sum_{j=0}^{N} M_{Lsj} \cdot \{ [\min(0, 15 - Ls_{j})]^{2} + [\min(0, Ls_{j})]^{2} \} \}$
 $i \in [0, 1,, n]$
 $j \in [1, 2,, m]$

One typical optimization result is as shown in Fig. (4). The dotted line in the figure are the simulation results, the solid line is the best incentive curve. The simulation's results show that the optimization values are in good agreement with the metrical values.

Set

7. CURRENT AND FUTURE DEVELOPMENTS

Current and near-term future work on microstrip equalizer is described above. Usually, the measured results are more realistic than the calculation results in engineering design. By the way of reasonably measuring and establishing database of the substructure, we can avoid repeated calculation of each substructure in the process of optimization. It is effective to advance the calculation efficiency. Finally, in a more long term perspective, the fast design of complex microstrip equalizer can be realized.

CONFLICT OF INTEREST

The authors confirm that this article content has no conflict of interest.

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