

Comparison Between Ultrathin Films of YSZ Deposited at the Solid Oxide Fuel Cell Cathode/Electrolyte Interface by Atomic Layer Deposition, Dip-Coating or Sputtering

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Abstract: The effect of an 80 nm YSZ interfacial layer, deposited onto a YSZ pellet by different techniques (dip-coating, sputtering and atomic layer deposition) at the SOFC cathode/electrolyte interface to allow a maximum adhesion of the thick cathode layer, was thoroughly analysed. The LSM cathode was deposited on the solid electrolyte by painting and sintered in air at 1200°C for 2 h. The morphological and structural analyses of the samples were performed by scanning electron microscopy and X-ray diffraction; their electrical properties were examined between 390 and 700°C by electrochemical impedance spectroscopy. The impedance responses showed three different contributions. The high-frequency arc is attributed to the YSZ electrolyte. The electrode processes associated with the medium- and low-frequency arcs were discussed. The electrochemical performance was influenced by the microstructure at the electrode/electrolyte interface.

Keywords: SOFC, thin layers, impedance spectroscopy, electrical properties, lanthanum strontium manganite (LSM), yttria-doped zirconia (YSZ).

1. INTRODUCTION

SOFCs devices present several advantages with respect to low temperature fuel cells, mainly in the field of co-generation and in the transport area as auxiliary power units. The development of these solid electrolyte systems is supported by intense research and demonstration activities in most of the important industrial countries. The planar configuration still suffers from many technical problems, but appears as the most attractive one for the future, with some remarkable advances on innovative fabrication techniques and a wide variety of electrode and electrolyte materials. Although the feasibility of SOFCs has been clearly demonstrated, the usual working temperature, around 800°C, involves the use of expensive ceramic interconnects and to a premature ageing of the components. Therefore, it appears crucial to decrease their operation temperature in order to reach, as a first step, some niche markets.

However, the increase in the lifetime at 600-700°C leads to an insufficient ionic conductivity of the electrolyte and to an increase in the electrode overpotentials. At these temperatures, it is essential to lower the electrolyte resistance

either by reducing the thickness of the usual electrolyte, i.e. yttria-stabilised zirconia, YSZ, or by finding new materials with a significantly higher conductivity. In any case, the use of thin layers cannot be ignored nowadays. Furthermore, in order to increase the electrochemical performance of the electrode materials and, in particular, on the cathode side (where the oxygen reduction overpotential is known to be high), the use of ultrathin interface layers over which would be deposited thick layers of the same material would reduce significantly the associated overpotentials.

Some of the present authors were among the first to privilege ultra thin electrolytes for SOFC application [1-6]. This key issue offers several opportunities; not only in order to lower the working temperature and use stainless steels interconnects, but also to avoid high temperature annealing treatments. Another fundamental aspect is to control the electrical conductivity and electrochemical reactions by optimising the thin-layered nanocrystalline structure. It is now admitted that nanomaterials present very often an unusual conductivity and thermal stability due to the high ratio of intergranular area with respect to the total grain area [7-12]. One should keep in mind that, in principle, the effective intergranular resistivity is a decreasing function of the grain size: the lower the grain size, the higher is the intergranular resistance. Nevertheless, nano-scaled thin layers may also have a beneficial impact on electrode reaction mechanisms, on thermal constraints and on cost reduction. New architectures, which include thin layers,

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appear capital for the efficient development of SOFC technology. Recently, several thin layers techniques have been dedicated to SOFCs devices, as described in an interesting paper, focussing on vacuum deposition or liquid-based techniques [7].

This work is dedicated to the optimisation of thin layers obtained by different techniques (dip-coating, sputtering and atomic layer deposition), to analyse their performance and compare them in different configurations. Our final goal is to use anode-supported cells, but as a first step, in order to optimise the cathode interfaces, we have concentrated our effort on the interfaces between YSZ electrolyte and LSM cathode. In the present paper, the first of a series of three papers, our purpose is to analyse the effect of an interfacial layer of YSZ (80 nm) deposited by the above-mentioned techniques and covered by a thick LSM layer deposited by painting. After depositing and characterising the structural and morphological properties of these layers, the main focus is given to the electrical and electrochemical behaviour of such systems including the mentioned ultrathin layers. One must keep in mind that this part aims essentially at comparing the techniques operated at given elaboration conditions and does not constitute a final goal. The originality is also in the variety of the thin layers developed in three independent laboratories; moreover, in order to have a rigorous comparison of the electrical properties of all the systems including such thin layers, all the impedance spectroscopy tests were realised in the same entity. As the knowledge of the specific behaviour of thin layers properties is still relatively poor, our contribution makes sense and will allow, at least, pointing out some crucial facts concerning nano-organised interfaces.

2. EXPERIMENTAL

Ultrathin layers of 80 nm were elaborated by three techniques in different laboratories:

- At the CIRIMAT by dip-coating.
- At the LSGS by reactive sputtering (PVD).
- At the LECIME by atomic layer deposition (ALD).

Impedance spectroscopy measurements were realised at the LECIME.

Thick contact layers were deposited by painting at the ICMCB.

2.1. Preparation of Electrolyte Substrates

The electrolyte substrates were prepared from an YSZ commercial powder containing 8 mol% of Y_2O_3 (TZ8Y from Tosoh Corp, Japan). The powder was pressed into pellets of diameter 19 mm and thickness of around 1 mm. They were sintered at 1350 °C for 2 h in air. After sintering, density of the pellet was up to 95%. Pellets were then polished mirror-quality with an average roughness of about 10-15 nm and then cleaned in successive ultrasonic baths of acetone and alcohol.

2.2. Preparation of Precursor and Deposition of YSZ Thin Film with Different Techniques

2.2.a. Thin Yttria-Stabilised Zirconia Deposited by Atomic Layer Deposition (ALD)

Thin films (80 nm) were deposited in a commercial flow-type hot-wall ALD reactor F-120 manufactured by ASM

Microchemistry Ltd. Film depositions were carried out at 1-3 mbar pressure. Commercial Cp_2ZrCl_2 (Strem Chemicals, #93-4002, 99%) was used as the zirconium precursor and $Y(thd)_3$ (Strem Chemicals, #39-1000, 98+% (99.9%-Y)) was used as the yttrium precursor. O_3 was used as oxidizer and it was generated from O_2 (99.999%) in an ozone generator (Fischer model 500 MM). Nitrogen (>99.999%, Schmidlin UHPN 3000 N_2 generator) was used as carrier and purging gas. Pulsing lengths varied between 1 and 2.5 s.

The standard pulsing sequence in one cycle combines two pulsing sequences of Cp_2ZrCl_2 (*Pulses 1-4*) followed by one pulsing sequence of $Y(thd)_3$ (*Pulses 5-8*). The number of deposition cycles determines the film thickness.

Pulse 1: Cp_2ZrCl_2 for 1 s

Pulse 2: N_2 purge for 1 s

Pulse 3: O_3 for 2.5 s

Pulse 4: N_2 purge for 1 s

Pulse 5: $Y(thd)_3$ for 1 s

Pulse 6: N_2 purge for 1 s

Pulse 7: O_3 for 2.5 s

Pulse 8: N_2 purge for 1 s

The deposition temperature was 300°C. The volatilisation temperatures of precursors were the following: 120°C for $Y(thd)_3$ and 130°C for Cp_2ZrCl_2 . The microstructure and the film thicknesses were evaluated from scanning electron microscopy (SEM) micrographs; an example is given in Fig. (1a, b).

2.2.b. Thin Yttria-Stabilised Zirconia Deposited by Sputtering (PVD)

80 nm-thick YSZ films were deposited at room temperature by sputtering a 16 at% Y-Zr target in a reactive Ar- O_2 atmosphere. The experimental device is a 40-litre sputtering chamber pumped down *via* turbo-molecular pump, allowing a base vacuum of 10^{-4} Pa. The 50 mm in diameter target was fixed on an unbalanced magnetron with its centre at 60 mm from the rotating substrate-holder axis. It was powered by an Advanced Energy system consisting of a 6 kW Pinnacle D.C. unit connected to a 20 kHz Spark-le pulser operated in active arc mode. The YSZ bulk pellets were placed with their centre at 40 mm from the holder axis. The argon and oxygen flow rates controlled with MKS flowmeters were set to 25 and 2.6 sccm respectively to run the process in the compound sputtering regime. The argon pressure measured using a MKS Baratron absolute gauge was close to 0.3 Pa. Before the deposition, a 50W radiofrequency plasma etching procedure was applied for 5 minutes to the YSZ substrate in order to create free radicals and consequently improve the film adhesion. This procedure was ended few seconds after the start of the deposition run. The discharge current during the deposition run was fixed to 0.5 A, the resulting discharge voltage was close to 320 V.

2.2.c. Thin Yttria-Stabilised Zirconia Deposited by Dip-Coating

Yttria stabilized zirconia precursor sols were prepared in the system consisting of zirconium n-propoxide, yttrium nitrate, n-propanol, acetylacetone (acacH) and water.

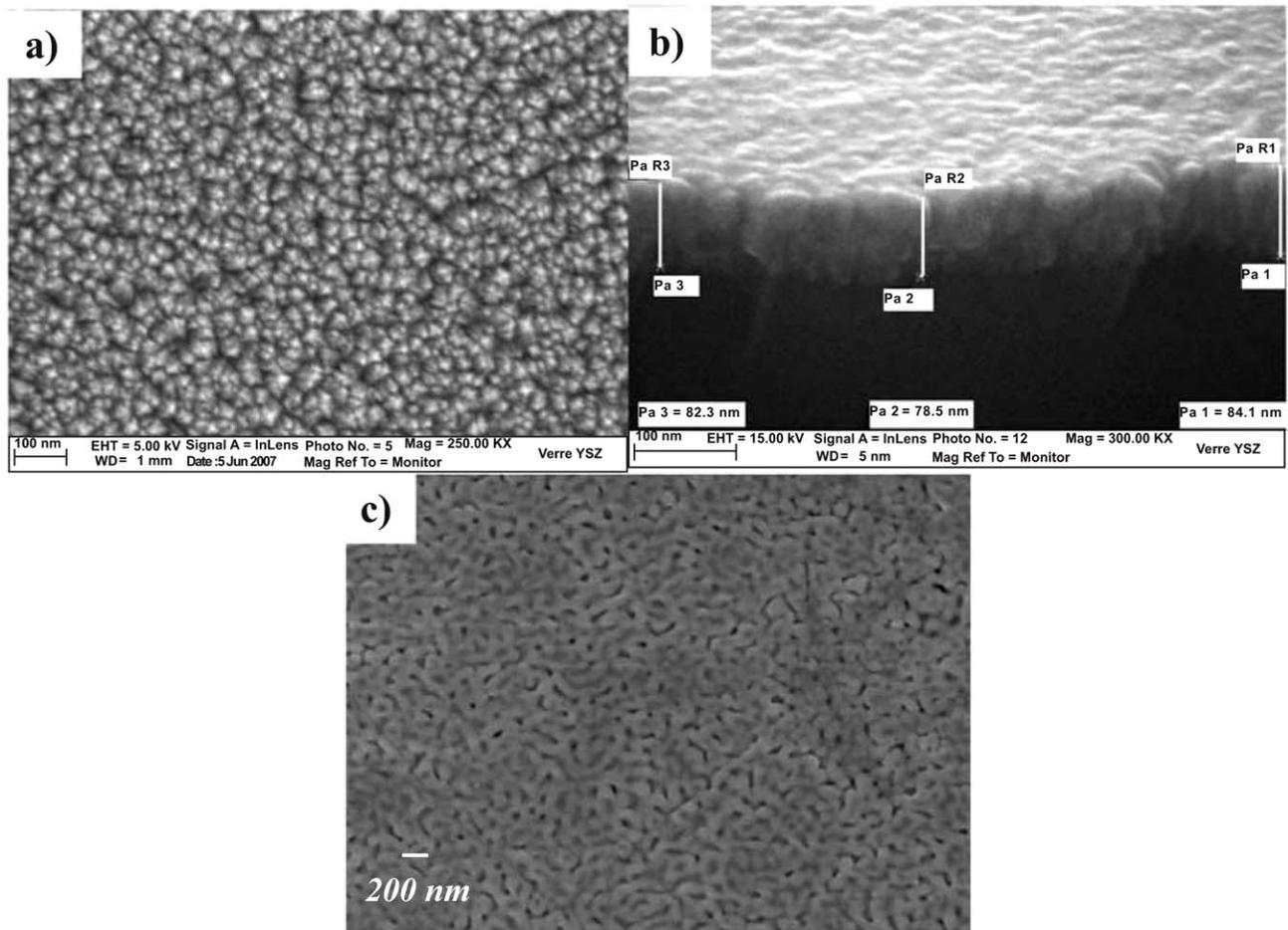


Fig. (1). SEM micrographs relative to YSZ thin layers deposited by ALD onto soda-lime glass (a) surface morphology, (b) cross-section and (c) surface morphology deposited by dip-coating onto YSZ substrate.

Commercial zirconium alkoxide (Aldrich #33,397-2, 70 wt% solution in 1-Propanol), yttriumIII nitrate hexahydrate (Acros Organics #13494-98-9, 99.9%), 1-Propanol (Acros Organics #71-23-8, +99%), acetylacetonone (Sigma-Aldrich #P775-4, +99%) and deionised water were used as starting precursors. Zirconium alkoxide being very reactive with water, the zirconium n-propoxide was first diluted with n-propanol and then chelated by acetylacetonone under neutral atmosphere, which allows controlling the hydrolysis and condensation reactions avoiding precipitation [13-14]. Then, a water/n-propanol mixture containing the yttrium salts, previously dissolved, was added under vigorous mechanical stirring. The zirconium n-propoxide concentration $C=[Zr(OC_3H_7)_4]$ was kept equal to 0.5 mol/L, the hydrolysis and complexing ratios being kept constant, $W'=[H_2O]/[Zr(OC_3H_7)_4+Y(NO_3)_3-6H_2O]=10$ and $R'=[acacH]/[Zr(OC_3H_7)_4+Y(NO_3)_3-6H_2O]=0.7$ respectively. The obtained sols are homogeneous, clear and transparent.

After dipping the YSZ polycrystalline polished mirror substrate into the sol and after withdrawing it at a controlled speed, a coating of yttria stabilized zirconia precursor sol was carried out under ambient conditions. During the dipping, the liquid film ran out on the substrate, adhered to its surface and the solvents evaporation led to its rapid

consolidation. In the dip-coating process, the as-prepared coating thickness is mainly depending on various synthesis and shaping parameters as the sol viscosity and density, the liquid-vapor surface tension and the withdrawal speed. Its behaviour generally evolves by a relationship derived by Landau and Levich [15]. The final thickness of the annealed coating also relies on the solid content, i.e. the zirconium alkoxide concentration and the annealing temperature. In this work, the synthesis sol parameters were kept constant in order to adjust the final thickness layer at 80 nm. So, the only adjustable parameter was the withdrawal speed which was kept at 1.8 cm/min. After a drying at 60°C during 20 minutes, the films were continuous, homogeneous and crack free as observed by optical microscopy. The dried films were thermal treated in an electric furnace at a rate of 100°C/h up to 1000°C with a two hours dwell time. X-ray diffraction measurements show after thermal treatment at 1000°C, the crystallisation of the precursor in the cubic yttria-stabilised zirconia phase. The relative intensities of the peaks being in good agreement with JCPDS files, the cubic yttria-stabilized zirconia crystals are randomly orientated. The *in situ* homogeneous crystallisation of the zirconia precursor led to relatively dense layer at 1000°C and constituted of a very narrow distribution of crystallised YSZ nanoparticle sizes (70 nm) as it can be seen in Fig. (1c).

2.3. Deposition of $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$ Cathode

Commercial submicronic powders were used for lanthanum strontium manganite cathode (LSM powder (20% strontium) from Nextech Materials Co. Ltd.). Cathode ink was prepared mixing the powder with ethanol or ethylene glycol. The slurry was painted onto the thin interlayer of YSZ by means of a brush. The green layer was annealed in air at 1200°C for 2 h to obtain a strong adhesion between the components. The whole electrode thickness was about 10–13 μm .

2.4. Morphological and Structural Analysis

The surface morphology and the microstructure of the cross-section of the samples were investigated using scanning electron microscope SEM (S440, FEG from LEICA). The thickness of the deposits was determined from cross-sectional SEM micrographs. Surface and cross sectional images were obtained from the samples YSZ/YSZ(dip-coating)/LSM, YSZ/YSZ(PVD)/LSM and YSZ/YSZ(ALD)/LSM. An X-ray diffraction analysis was assessed on the sputtered film using a Co K_α radiation ($\lambda = 0.178897 \text{ nm}$) in glancing angle condition (2°).

2.5. Electrical Measurements

Electrical measurements were performed by impedance spectroscopy, using a PGSTAT 20 potentiostat, Autolab Ecochemie BV. (Amsterdam, the Netherlands). Impedance spectroscopy measurements were carried out under ambient atmospheric air, as a function of the temperature. A specific two-electrodes asymmetric configuration was used as depicted in Fig. (2) [4]. Even if non-ideal, this electrochemical configuration, with a Pt point electrode, was used for all the samples. It allows a reasonable comparison of their properties in similar conditions. With respect to the system linearity, 50 mV ac signal amplitude was applied, without DC polarisation. Each impedance measurement was carried out in the 1 MHz–10 mHz frequency range, with 11 points per decade. The temperatures varied from 390°C to 700°C. The numbers shown in the impedance diagrams

indicate the logarithm of the measuring frequency. The impedance data are deconvoluted using the software EQUIVCRT [16].

3. RESULTS AND DISCUSSION

3.1. Morphological and Structural Analysis

Fig. (3) shows the surface morphology and the cross-section of the fractured surface for the three samples, including the YSZ interfacial layer deposited by dip-coating, ALD or PVD. The LSM thick films adhered well to the dense electrolyte. An homogeneous distribution of pores and particles can be observed over the whole LSM thickness. Although the thick cathode layers were sintered at the same temperature (1200°C for 2 hours), their microstructure was slightly different (Fig. 3b, d, f). The origin of such modification is difficult to elucidate: it does not seem likely to be due to a difference in the surface state of the thin YSZ layer (80 nm) over which is deposited the thick layer of LSM cathode. No precise explanation can be given at this stage, apart maybe the chemical reactivity between both layers, which may be influenced by the YSZ deposition technique. For all the samples, the dense YSZ substrate is clearly visible and it is possible to distinguish the porous from the dense phase (Fig. 3a, c, e). The thicknesses of LSM, determined from the SEM, micrograph are in the range of 10 to 13 μm .

In order to estimate the thickness and the average grain size of the YSZ(ALD) layer, the film was deposited in the same conditions on soda-lime glass (Fig. 1a, b); the particles size of the deposited YSZ is around 18-25 nm and the thickness of the thin layer is in the range of 78-84 nm. In the case of the YSZ(PVD) layer, the film was deposited in the same conditions on an amorphous substrate, soda-lime glass, and a crystalline isostructural one, ceria-doped gadolinia (CGO). The X-ray diffraction patterns of the film deposited on soda-lime glass is presented in Fig. (4). Broad diffraction peaks characteristic of YSZ are present (JCPDS, ref. 30-1468). The average grain size was estimated from the Scherrer formula in the range of 7-11 nm, either on glass or

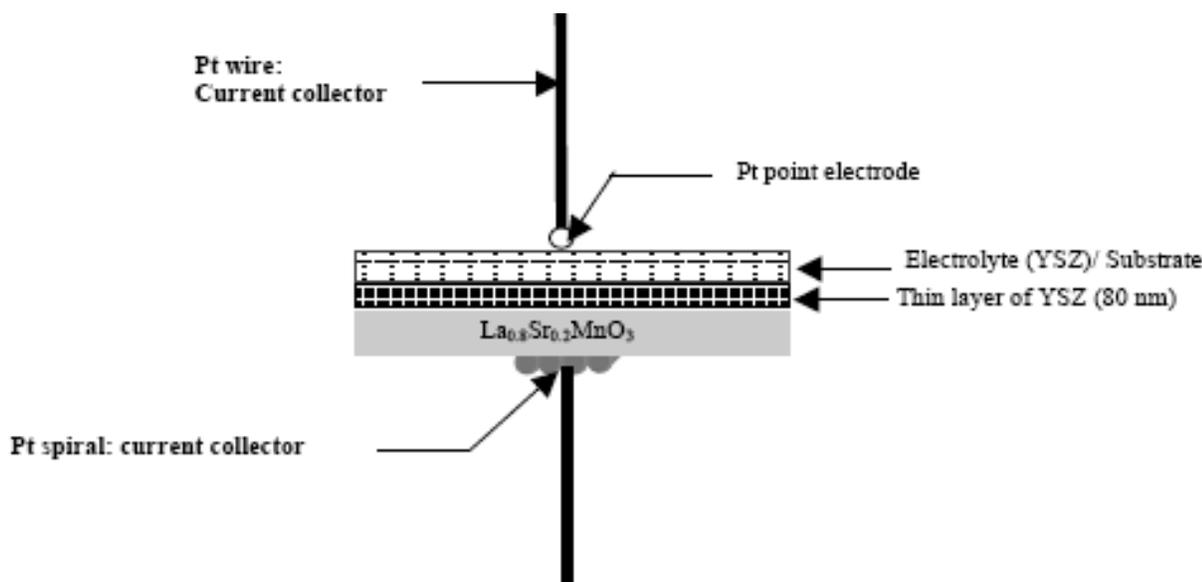


Fig. (2). Scheme of the measurement device used for the impedance spectroscopy measurements [4].

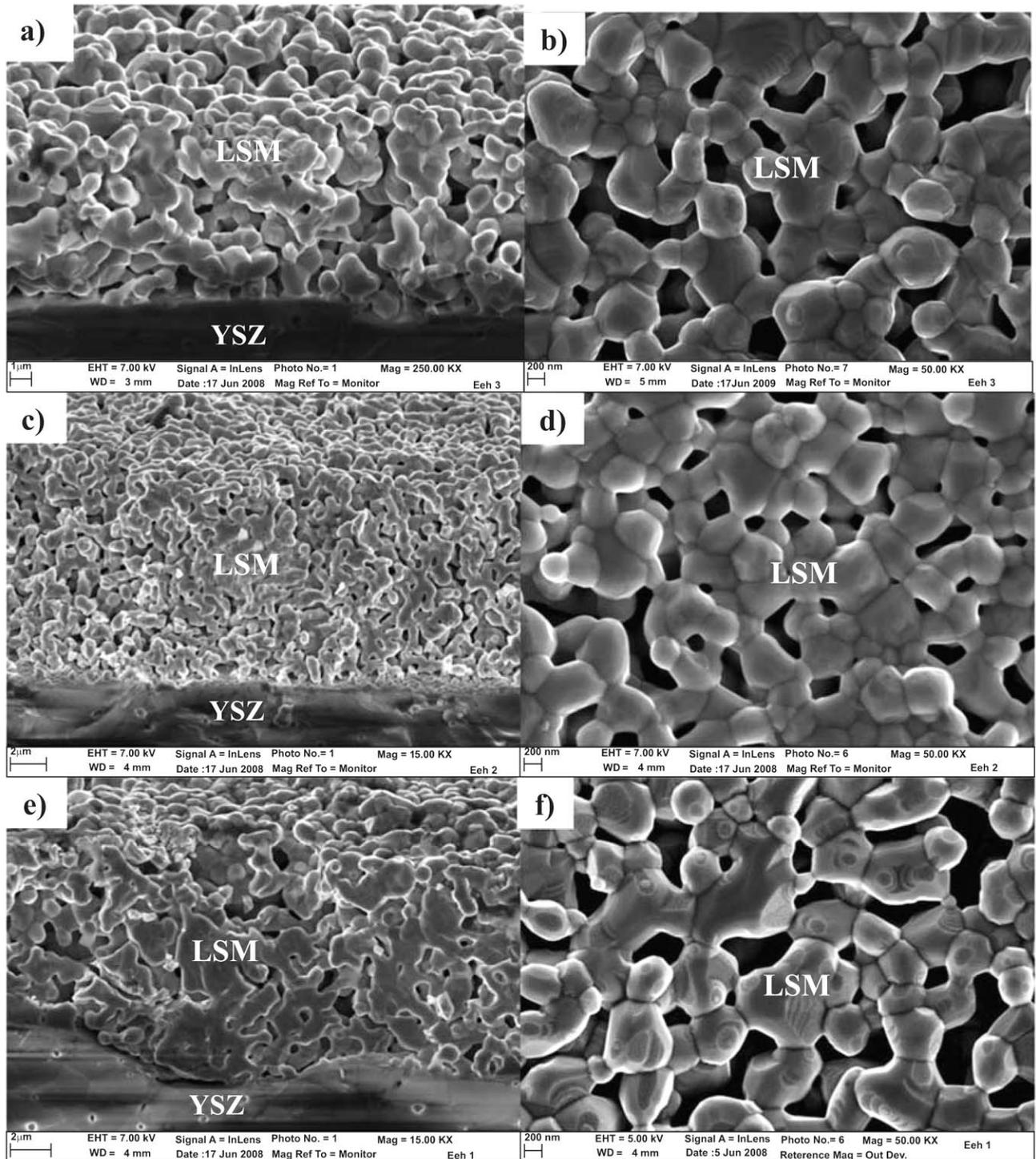


Fig. (3). SEM micrographs relative to surface morphology and cross-section of (a, b) YSZ/YSZ(dip-coating)/LSM (c, d) YSZ/YSZ(PVD)/LSM and e, f) YSZ/YSZ(ALD)/LSM samples.

CGO substrates [17], which highlights the very limited influence of the substrate chemical and structural state within the deposition conditions explored in the present study. The thickness of the coatings was obtained by fitting the interferometric measurements obtained by Optical Transmission Interferometry (OTI) with a simple Matlab simulation program, following a theory developed elsewhere

[18-20]. Coating thickness was also determined *ex-situ* by optical transmittance measurements performed with a UV-visible-NIR Carry 5000 Varian Spectrophotometer, using a software developed at the laboratory with the assistance of PVDco (a French SME). These measurements were systematically confirmed using the step method with a Talysurf profilometer.

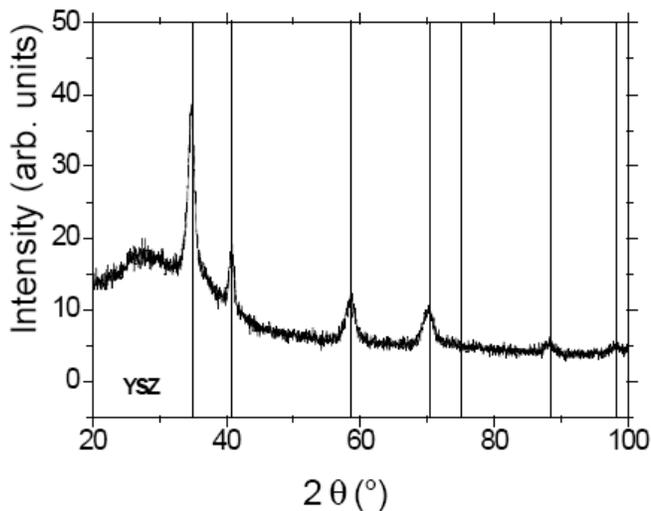


Fig. (4). X-ray diffractogram of an 80 nm YSZ layer deposited on soda-lime glass substrate. The vertical lines represent the theoretical positions of the YSZ diffraction peaks.

The YSZ thin film layers deposited by dip-coating, PVD or ALD between the YSZ substrate and the LSM thick film

are shown in some detail in Fig. (5). According to the SEM micrographs, there is a difference between the thin layer of YSZ deposited by PVD and the two other techniques. The thin layer is more visible and well-bonded to the YSZ substrate when it deposited by ALD or dip-coating. The YSZ interlayer prepared from PVD is not clearly visible and has probably reacted with the LSM layer. It is well known that the fine electrode microstructure close to the electrolyte would induce a large triple phase boundary (TPB) length and facilitate the charge transfer reaction and the coarser microstructure and porosity of the thicker outer cathode layer would facilitate the gas transport. For the YSZ thin layer deposited by ALD, fine particles of LSM are observed near the interlayer. This can be explained by the effect of the YSZ interlayer surface on the morphology of the LSM layer when it is coated.

3.2. Impedance Diagrams

The impedance responses measured at low and high temperature for the different cathode/electrolyte systems are presented in Figs (6, 7), respectively. The impedance diagram shapes are different. At low temperature, the semi-circle observed at the high frequency range is well-defined (i.e. up to 10 kHz at 450°C) for the three samples, but at the

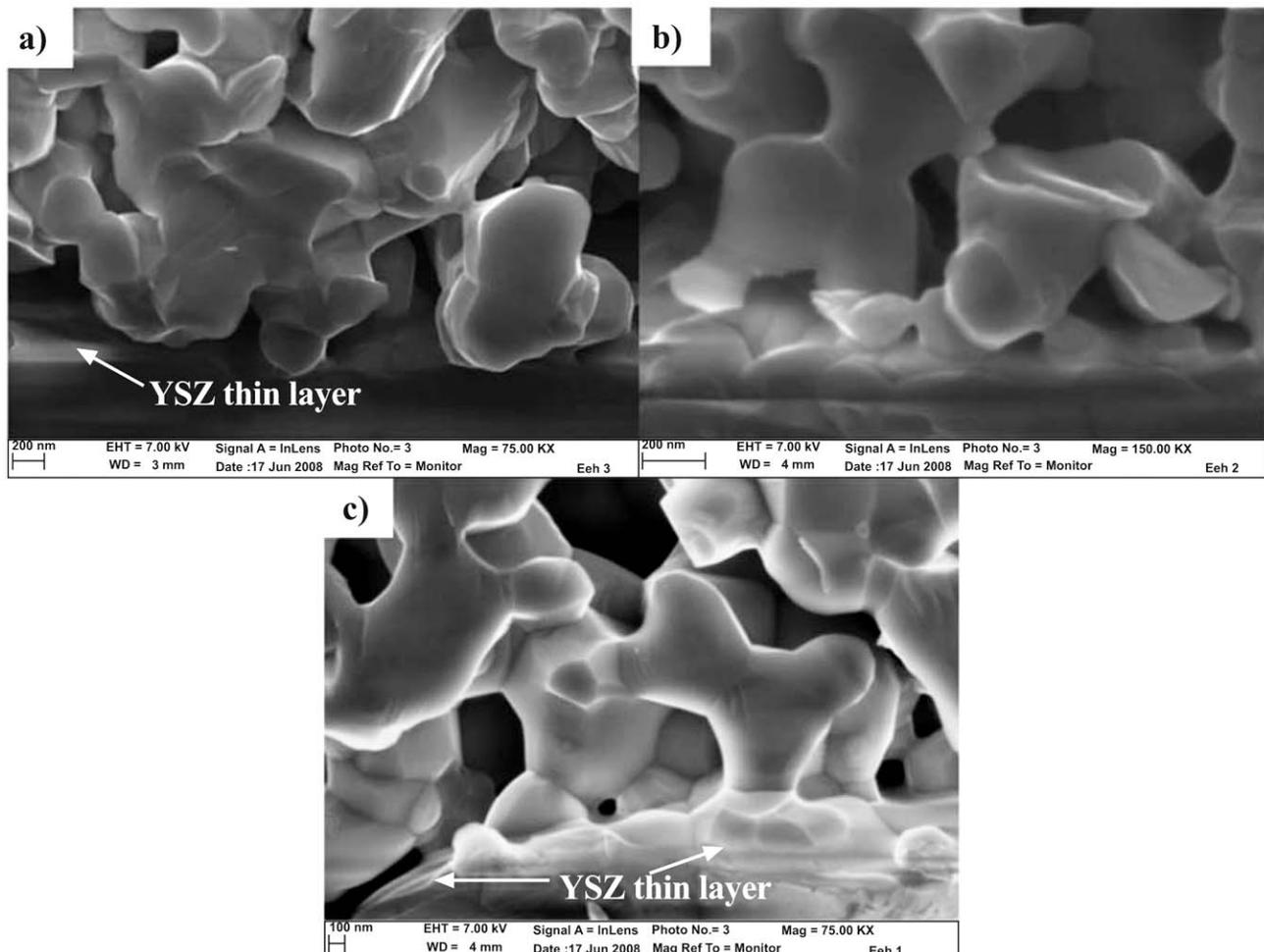


Fig. (5). SEM micrographs of the cross-sections of (a) YSZ/YSZ(dip-coating)/LSM (b) YSZ/YSZ(PVD)/LSM and (c) YSZ/YSZ(ALD)/LSM samples.

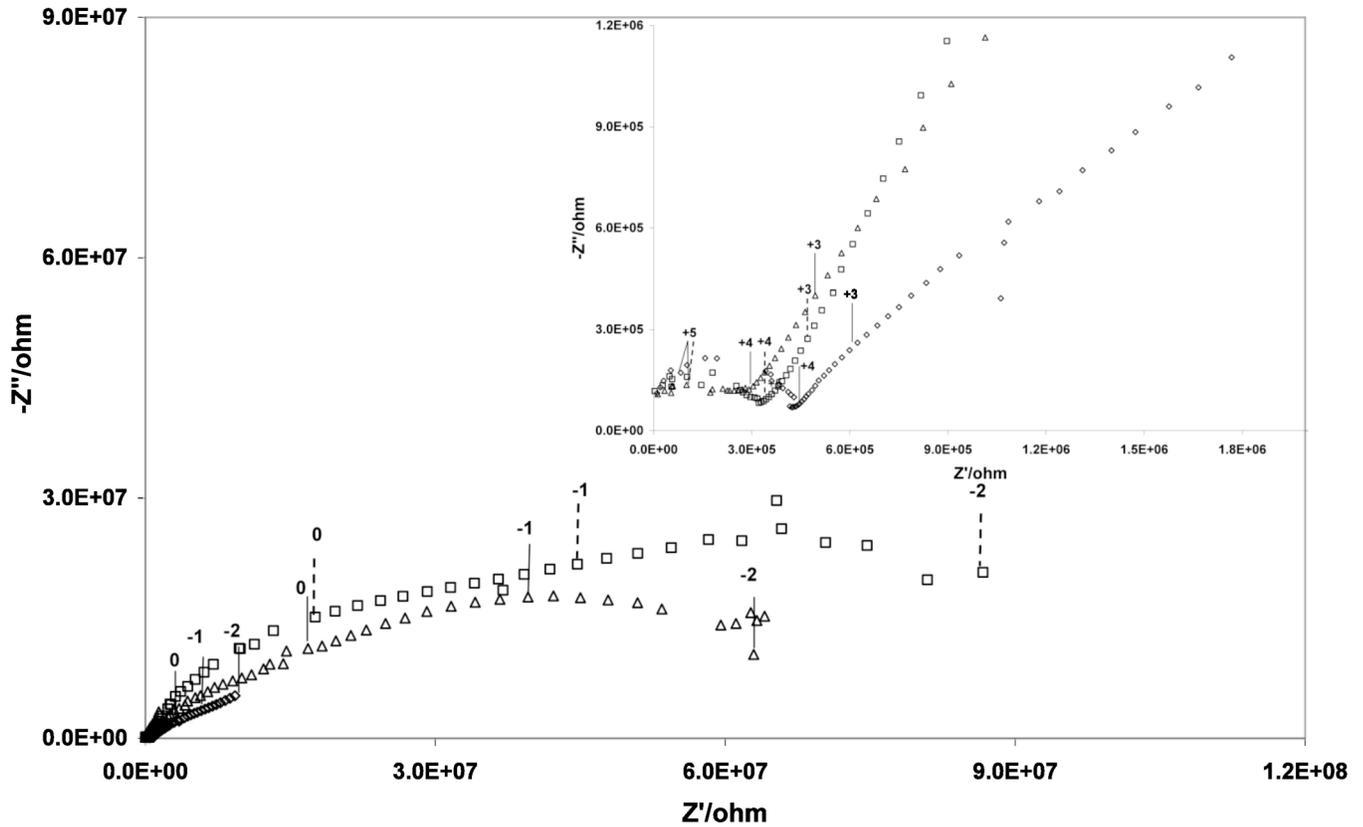


Fig. (6). Impedance diagrams measured at 450 °C for Δ)YSZ/YSZ (PVD)/LSM, □)YSZ/YSZ(dip-coating)/LSM and ◇)YSZ/YSZ (ALD)/LSM. Frequency logarithms are indicated.

lowest frequencies the responses are not fully defined in the frequency range used. The sample with the YSZ thin film deposited by ALD presents a higher resistance at high frequencies compared to the two other samples. At high temperature, the impedance spectra have a different shape. The first semi-circle, in the high frequency range, becomes smaller (i.e. up to 100 kHz at 660°C), whereas, the responses at medium and low frequencies become predominant. In the Bode diagram (Fig. 7b), three phenomena can be observed at high, medium and low frequencies. The samples with the interlayer prepared by dip-coating and PVD have the same shapes with a small response for the phenomenon appearing at medium frequencies (100-0.5 kHz) and a large and predominant response for the phenomenon observed at low frequencies (500-0.01 Hz). Contrarily, the sample with YSZ (ALD) layer has a large response at medium frequencies (50-0.01 kHz).

The best equivalent circuit fitting comprises a series of three parallel combinations of resistance and a constant phase element, CPE, as shown in Fig. (8). In the circuit, R_{HF} is the ohmic resistance of the electrolyte, R_{MF} and R_{LF} related to the electrode interface resistances at medium and low frequencies. CPE_{MF} and CPE_{LF} are the corresponding constant phase elements. CPE is an admittance $Y=Y_0(j\omega)^n$, where n is the frequency exponent and ω is the angular frequency. The arc summit frequencies were calculated using the formula $f^\circ = 1/(2\pi(RY_0)^{1/n})$ and associated capacitances using the formula $C = 1/(2\pi Rf^\circ)$, where f° is the relaxation frequency [21].

The selected circuit was used to fit the observed impedance diagrams. An example on the YSZ/YSZ(dip-coating)/LSM sample is shown in Fig. (9). In this figure, the symbols are the experimental data and the lines are the fitted values using the equivalent circuit of Fig. (8). The fittings between the experimental data and the calculated data are generally good. The error in the resistance evaluated by the software (EQUIVCRT) was $\pm 3\%$.

The high frequency semicircles, HF, were attributed to the YSZ electrolyte. The response of the electrolyte corresponds to both YSZ substrate and YSZ thin film deposited by ALD, dip coating or sputtering. The capacitances relative to the high frequency phenomenon (Fig. 11) are in the order of 10^{-12} - 10^{-11} F, showing no significant influence of the thin layer of YSZ.

Arrhenius plots of the electrolyte conductivity are shown in Fig. (10). The activation energies deduced from the Arrhenius relation, are given in Table 1.

The activation energies of the electrolyte response (HF) for the different cathode/electrolyte systems, determined from the Arrhenius diagrams, are almost the same, showing again the weak influence of the YSZ thin layer. The values are between 1.12 and 1.17 eV. These results are in agreement with the values reported in the literature [22]. However, it can be noted that the conductivity of the YSZ/YSZ(ALD) layer is slightly lower than that corresponding to the other systems.

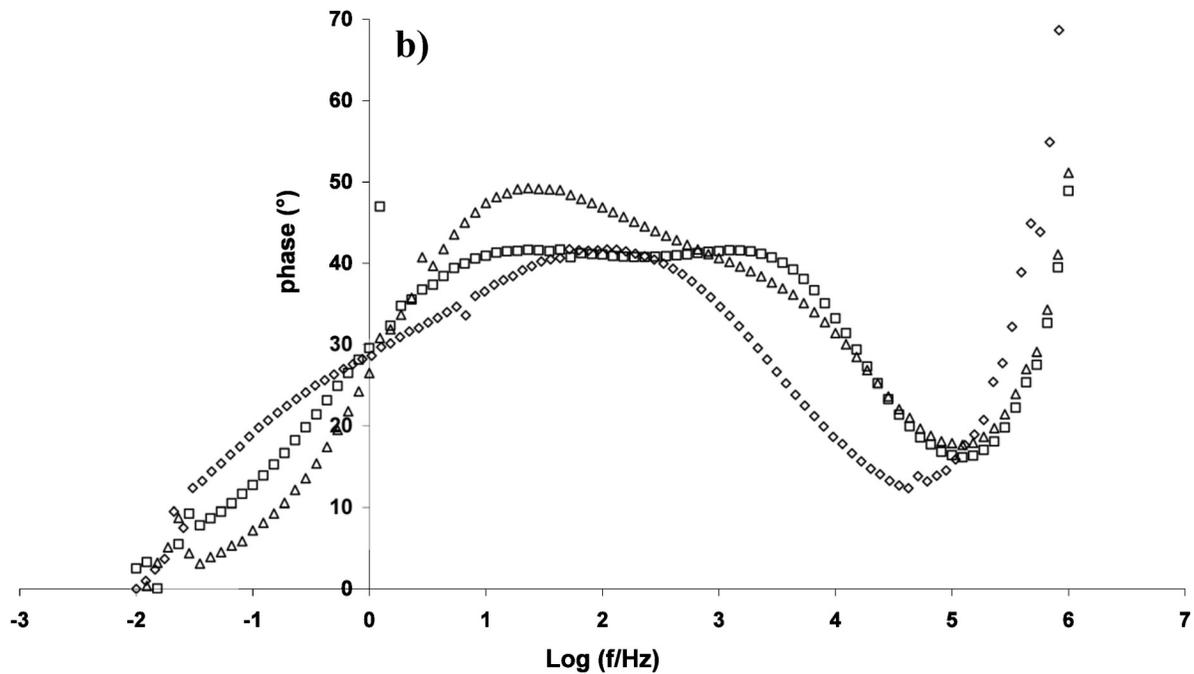
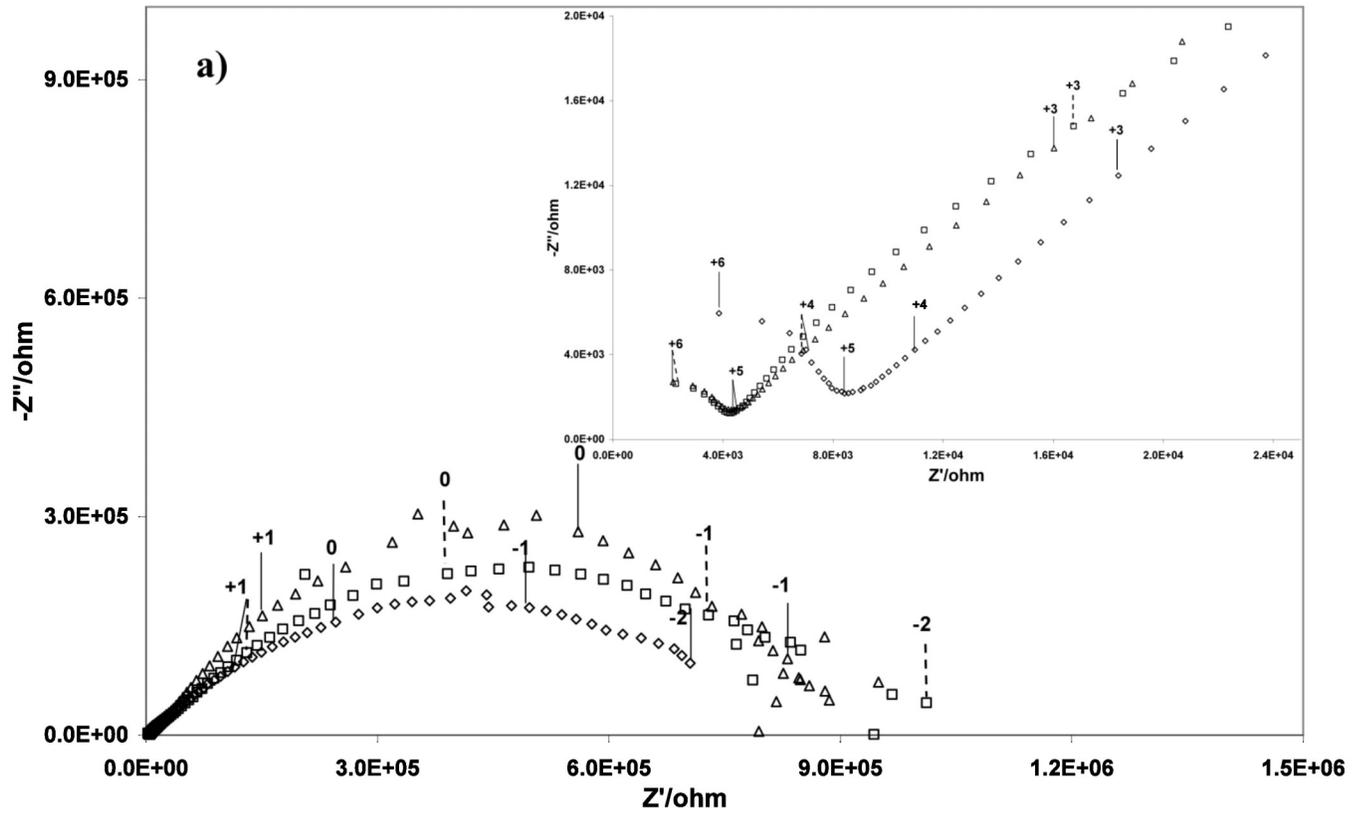


Fig. (7). Impedance diagrams measured at 660 °C for Δ)YSZ/YSZ (PVD)/LSM, \square)YSZ/YSZ(dip-coating)/LSM and \diamond)YSZ/YSZ(ALD)/LSM. **a)** Nyquist diagram (frequency logarithms are indicated), **b)** Bode diagram.

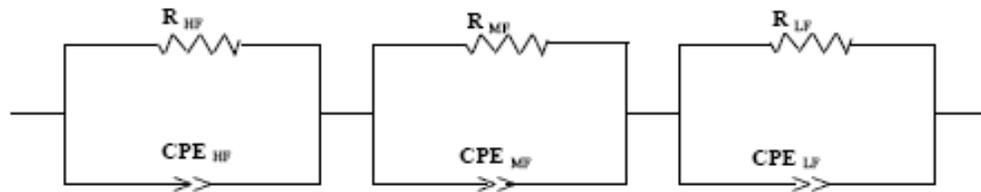


Fig. (8). Equivalent circuit used for fitting Impedance diagrams of samples.

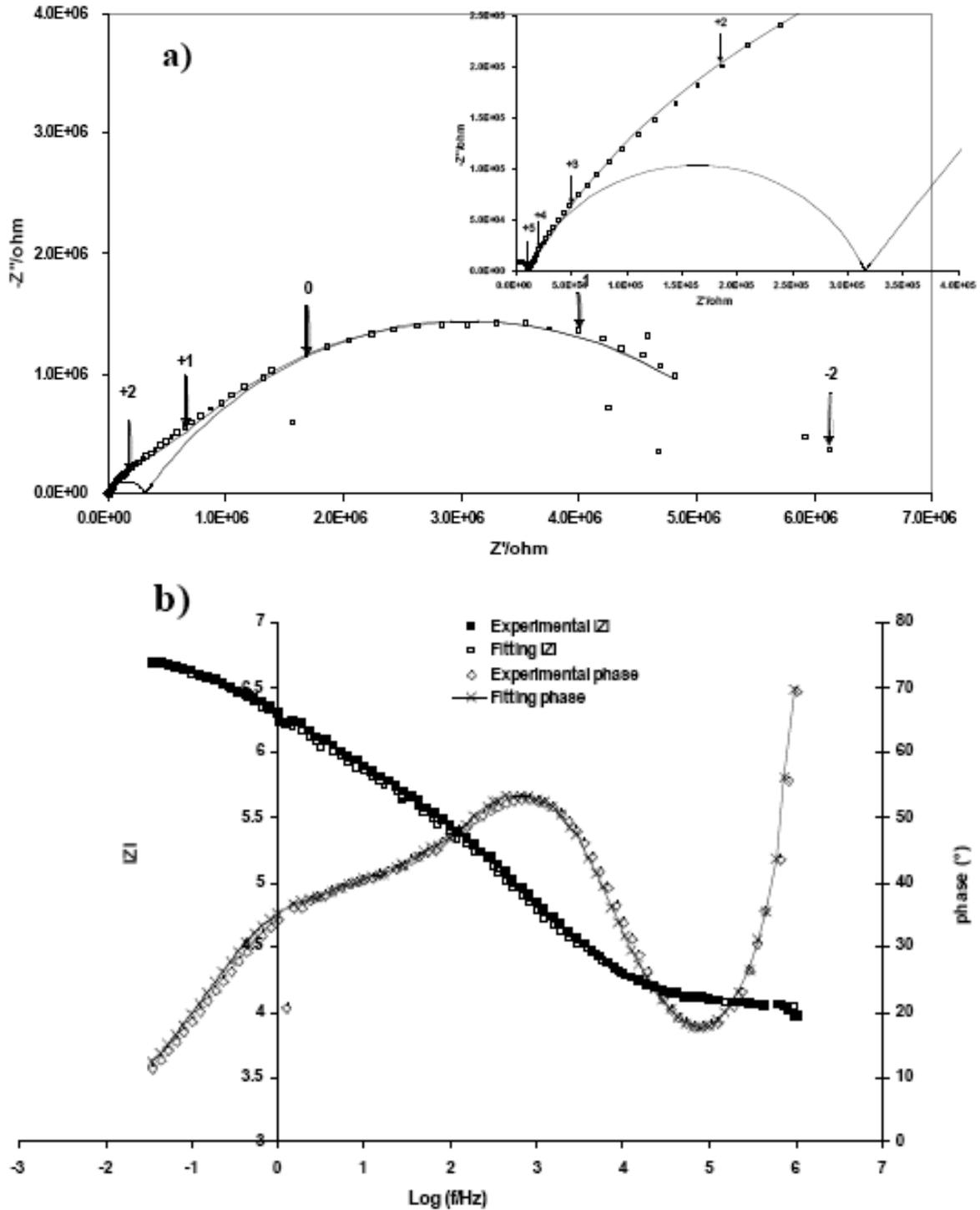


Fig. (9). Fitted results of experimental data measured at 600 °C for YSZ/YSZ(dip-coating)/LSM sample. (□) Experimental data; lines are the fitted results (a) Nyquist diagram and (b) Bode diagram.

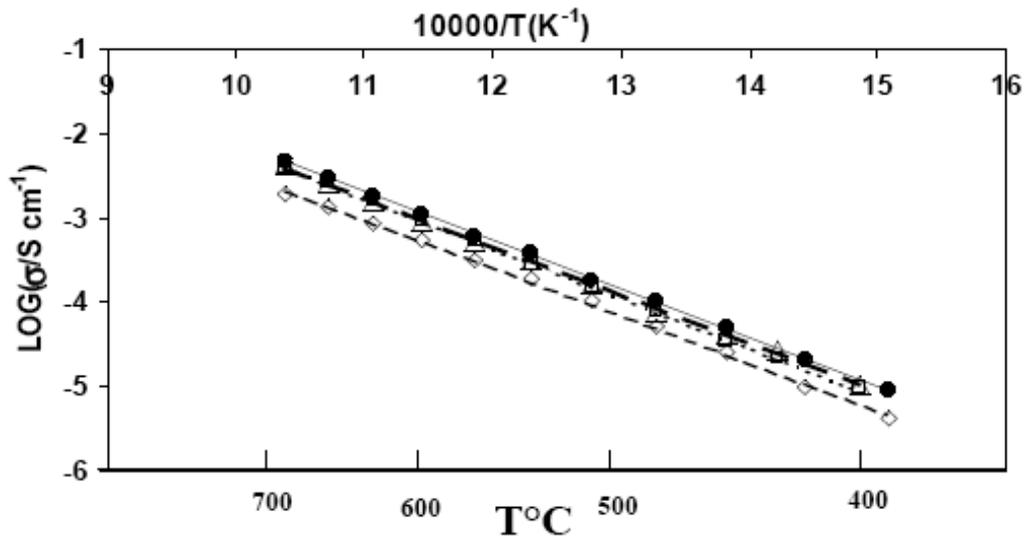


Fig. (10). Arrhenius plots of the electrolyte conductivity (HF) for Δ) YSZ/YSZ(PVD)/LSM, \square) YSZ/YSZ(dip-coating)/LSM, \diamond) YSZ/YSZ(ALD)/LSM and \bullet) YSZ/Pt.

Table 1. The Activation Energy of the Electrolyte Response (HF) for the Different Electrolyte /Cathode Systems

| Electrolyte/Cathode | Ea (eV) |
|--------------------------|---------|
| YSZ/YSZ(PVD)/LSM | 1.14 |
| YSZ/YSZ(dip coating)/LSM | 1.17 |
| YSZ/YSZ(ALD)/LSM | 1.12 |
| YSZ/Pt | 1.14 |

The LSM electrode behaviour was characterised by two arcs at medium and low frequencies (Fig. 9). This indicates that O_2 reduction on the LSM electrode is controlled by at least two different processes. The reaction mechanism of oxygen reduction involves a series of elementary reactions steps and still relatively little known. It is generally accepted that the overall reaction is composed of a multi-step reaction. In the literature [23-26], different interpretations were

reported on the impedance behaviour of the electrode processes at medium frequencies (O_2 reduction reactions). Youngblood *et al.* [26] attributed the medium frequencies arc to the charge transfer process. Jiang *et al.* [23] associated the electrode processes at medium frequencies with the migration and diffusion of oxygen ions (O^{2-}) from the three-phase boundary (TPB) region to the oxide ions vacancies located at the zirconia electrolyte lattice, the process being limited by the electrolyte conductivity. The semi-circle at medium frequency, MF, represents 17 and 18% of the total electrode resistance at 450°C (low temperature) for the YSZ/YSZ(dip-coating)/LSM and YSZ/YSZ(PVD)/LSM systems, respectively. This ratio decreases, respectively, to 1 and 2 % at 690°C (Fig. 12), which means that the impedance responses at low frequencies are more predominant at high temperature. This is not observed for the YSZ/YSZ (ALD)/LSM sample, where the ratio of the resistances $R_{MF}/(R_{MF}+R_{LF})$ evolves very slightly with the temperature. The resistance MF represents about 10% of the total electrode resistance, whatever the temperature. There is generally a common consensus that the semicircle at lower

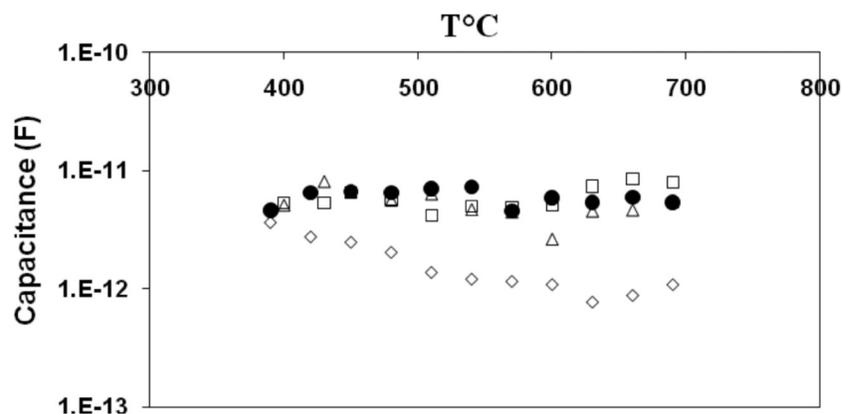


Fig. (11). Plots of the capacitance of the electrolyte response (HF) for Δ) YSZ/YSZ(PVD)/LSM, \square) YSZ/YSZ(dip-coating)/LSM, \diamond) YSZ/YSZ(ALD)/LSM and \bullet) YSZ/Pt.

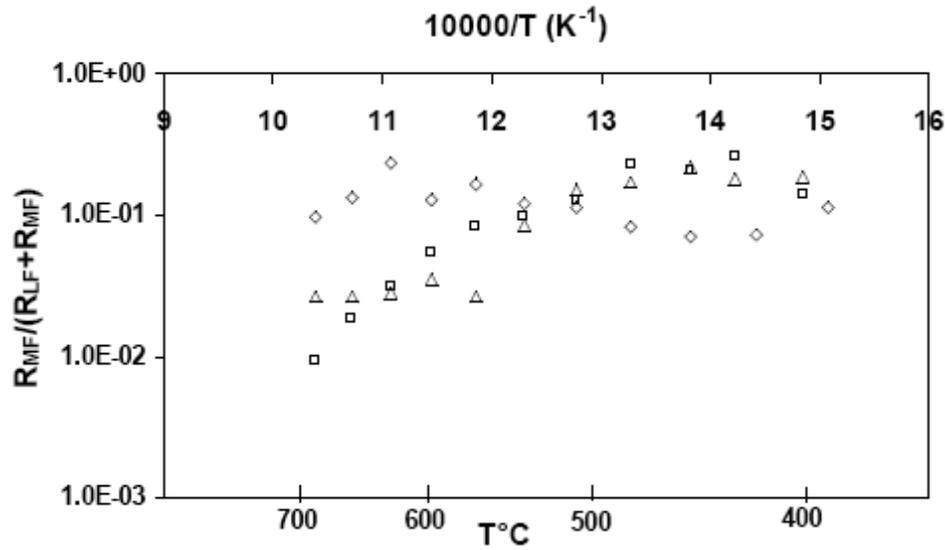
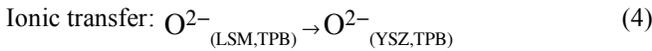
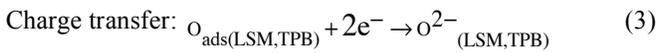
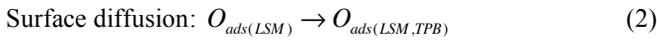
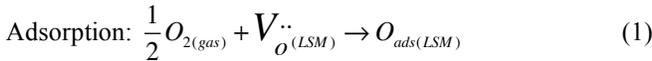


Fig. (12). Plots of the ratio ($R_{MF}/(R_{MF}+R_{LF})$) versus the temperature (HF) for Δ) YSZ/YSZ(PVD)/LSM, \square) YSZ/YSZ(dip-coating)/LSM and \diamond) YSZ/YSZ(ALD)/LSM.

frequency, LF, is attributed to the dissociative adsorption and diffusion processes of oxygen on the porous LSM electrode surface. According to S.P. Jiang *et al*, the reaction steps of the electrode processes at the low frequency arc could be summarized as follows [25]:



The Arrhenius plots of the polarisation resistance as a function of the temperature for different systems, depicted in Fig. (13), clearly shows that the YSZ/YSZ(ALD)/LSM

system has the smallest polarisation resistances. The presence of the ALD-built YSZ interlayer improves the interfacial resistance of the LSM/YSZ system, despite a lower conductivity of the ionic system. This result clearly demonstrates that the polarisation resistance depends strongly on the thin interlayer nanostructure and on the intimate phase contacts between this thin layer and the cathode, which can be considered as the three-phase boundary, including the gas phase pores. Sasaki *et al*. [27] already suggested that the performance of the cathode is closely correlated to the length of the three-phase boundary (TPB). The change in the activation energy of the cathodic process for the three samples is difficult to explain and still not completely clear but indicates that a modification in the limiting step of the process occurs.

The activation energies relative to the electrode response are given in Table 2. Y. Jiang *et al*. [29] explained the

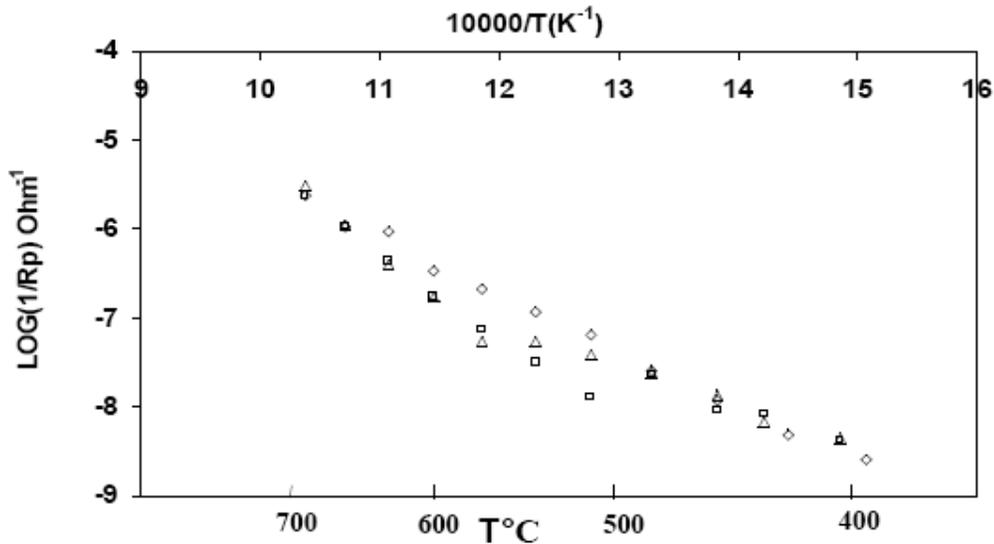


Fig. (13). Arrhenius plots of the polarisation resistance for Δ) YSZ/YSZ(PVD)/LSM, \square) YSZ/YSZ(dip-coating)/LSM and \diamond) YSZ/YSZ(ALD)/LSM.

Table 2. The Activation Energy of the Electrode Response (LSM) for the Different Electrolyte/Cathode Systems

| Electrolyte /Cathode | Ea (eV) | | Ea (eV) Literature on LSM | [Refs.] |
|--------------------------|---------|-----------|------------------------------------|---------|
| YSZ/YSZ(PVD)/LSM | 1.92 | T°≥500 °C | 1.61±0.05 | [28] |
| | 1.00 | T°<500 °C | 1.60 T°≥ 700 °C and 1.24 T°< 700°C | [29] |
| YSZ/YSZ(dip-coating)/LSM | 1.92 | T°≥550 °C | 1.99 | [30] |
| | 1.00 | T°<550 °C | 1.87 | [31] |
| YSZ/YSZ(ALD)/LSM | 1.28 | | 1.20-2.00 | [32] |
| | | | 1.04 | [33] |

change in the activation energies between the high and low temperature region by the shift of the rate-determining step from one step to another as the temperature increases. It can be noted that the LSM electrode response reported in the literature presents values of activation energy between 1-2 eV as it can be seen in Table 2, which is in agreement with those obtained in this study.

CONCLUSIONS

In this work, different techniques for preparing ultrathin YSZ interlayer were tested and used as a layer beaming for the thick layer of cathode deposited by painting. The micro, nanostructure and electrical conductive properties of YSZ/LSM system were investigated. This study demonstrated that the YSZ thin film deposited by dip-coating or ALD is more visible and well-bonded. However, the YSZ thin layer prepared from PVD is not clearly visible and has probably reacted with the LSM layer. The presence of a thin YSZ interlayer deposited by atomic layer deposition has a beneficial effect of the interfacial resistance of the YSZ/LSM system compared to the thin YSZ interlayer deposited by dip-coating or sputtering. The electrical measurements by impedance spectroscopy showed that the activation energies of the YSZ electrolyte (including the pellet and the thin layer) are in agreement with that reported in the literature for bulk YSZ.

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